ROHS

Transient Voltage Suppressors for ESD Protection

Description

The ESD2.5V88D-CDN is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium.

Features

- ◆ 150 Watts Peak Pulse Power per Line (tp=8/20µs)
- ◆ Protects One Bidirectional I/O Line
- Low Clamping Voltage
- Working voltages : 2.5 V
- Low leakage current
- ◆ IEC61000-4-4 (EFT) 40A (5/50ns)
- ♦ IEC61000-4-5 (LIGHTING) 17A (8/20µs)
- ◆ IEC61000-4-2(ESD):±30kV (air discharge) ±30kV (contact discharge)

Applications

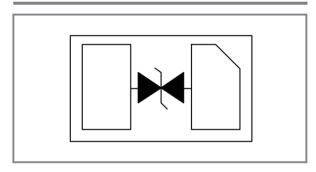
- Cell Phone Handsets and Accessories
- Microprocessor based equipment
- Notebooks, Desktops, and Servers
- Portable Instrumentation
- Peripherals
- Personal Digital Assistants
- Keypads, Side Keys, Audio Ports
- Digital Lines



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Functional Diagram



Mechanical Data

- SOD-882/DFN1006 (1.0x0.6x0.5mm) Package
- Molding Compound Flammability Rating: UL 94V-O
- Weight 0.5 Milligrams (Approximate)
- ◆ Lead Finish : Lead Free

Mechanical Characteristics

Parameter	Symbol	Value	Units
Peak Pulse Power (Tp=8/20µs waveform)	Ppp	150	Watts
Lead Soldering Temperature	T∟	260 (10 sec.)	°C
Storage Temperature Range	Тѕтс	-55 to +150	°C
Operating Junction Temperature Range	TJ	-40 to +125	°C



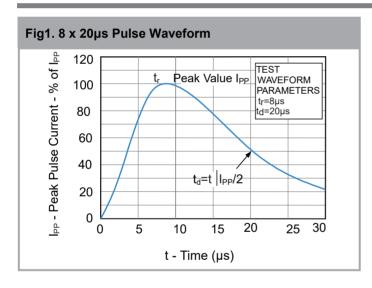
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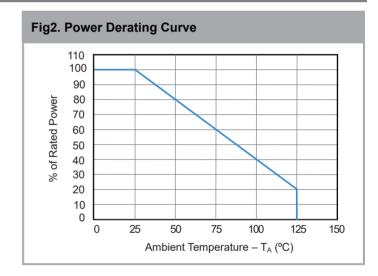
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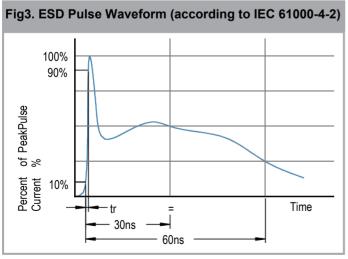
Electrical Characteristics @ 25°C Unless Otherwise Specified)

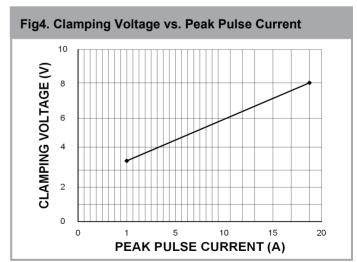
Characteristics	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Reverse Working Voltage	VRWM				2.5	V
Reverse Breakdown Voltage	VBR	I _T =1mA;	3.2			V
Reverse Leakage Current	lr	V _{RWM} =2.5 V, T=25°C;			0.5	μA
Clamping Voltage	Vc	I_{PP} =1A T_P = 8/20 μ s;			4.2	V
	VC	I_{PP} =17A, T_P = 8/20 μ s;			8.8	V
Junction capacitance	Сл	$V_R = 0 V, f = 1MHz;$		23		pF

Characteristic Curves







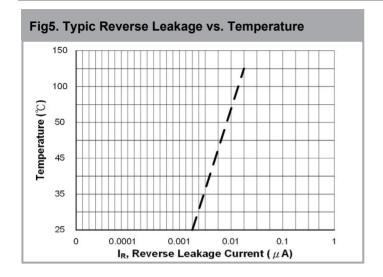




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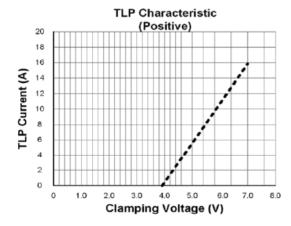
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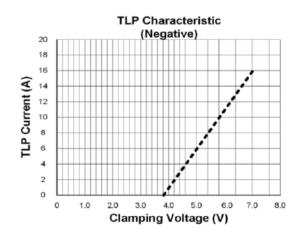
Characteristic Curves



Transmission Line Pulse (TLP)

Transmission Line Pulse (TLP) is a measurement technique used in the Electrostatic Discharge (ESD) arena to characterize performance attributes of devices under ESD stresses. TLP is able to obtain current versus voltage (I–V) curves in which each data point is obtained with a 100ns long pulse, with currents up to 40 A. TLP was first used in the ESD field to study human body model (HBM) in integrated circuits, but it is an equally valid tool in the field of system level ESD. The applicability of TLP to system level ESD is illustrated in Figure 1, which compares an 8 KV IEC 61000–4–2 current waveform with TLP current pulses of 8 and 16 A. The current levels and time duration for the pulses are similar and the initial rise time for the TLP pulse is comparable to the rise time of the IEC 61000–4–2's initial current spike. This application note will give a basic introduction to TLP measurements and explain the data sheet parameters extracted from TLP for SDI Technology's protection products





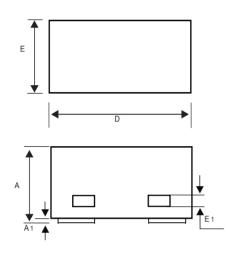


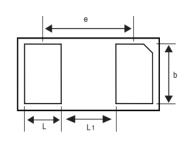
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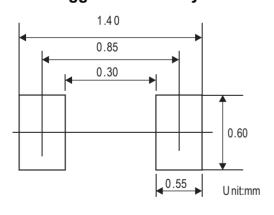
SOD-882/DFN1006 Package Outine & Dimensions

SOD-882/DFN1006





Suggested PAD Layout



Symbol	Millimeters				
Symbol	Min.	Nom	Max.		
Α	0.450	0.500	0.550		
A1	0	0.020	0.050		
E1	0.013	0.063	0.113		
D	0.900	1.000	1.100		
Е	0.500	0.600	0.700		
е	0.65BSC				
L	0.150	0.250	0.350		
b	0.400	0.500	0.600		
L1	0.300	0.400	0.500		

Ordering Information

Device	Marking	Package	Quantity	Reel Size
ESD2.5V88D-CDN	TF	SOD-882/DFN1006	12,000pcs/Ree	7 inch



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