

UN200N72TE

N-Channel Enhancement Mode MOSFET

ROHS

Product Summary

V _{DS}	20V
I _D	0.75A
R _{DS(ON)} (@V _{GS} =4.5V I _D =0.65A)	≤330mΩ
R _{DS(ON)} (@V _{GS} =2.5V I _D =0.45A)	≤400mΩ
R _{DS(ON)} (@V _{GS} =1.8V I _D =0.25A)	≤750mΩ

Features

- ◆ Advanced Trench Process Technology
- ◆ Low Threshold Voltage
- ◆ Fast Switching Speed
- ◆ Halogen-Free & Lead-Free
- ◆ N-Channel Switch with Low R_{DS(ON)}

Applications

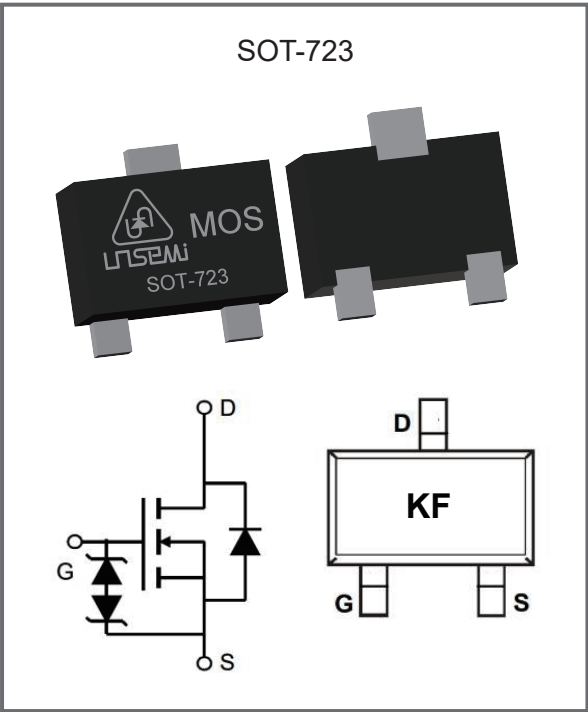
- ◆ Load Switch for Portable Devices
- ◆ Voltage controlled small signal switch

Package Marking And Ordering information

Part Number	Package Type	Packaging	Reel(pcs)
UN200N72TE	SOT-723	Tape & Reel	8000



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Absolute Maximum Ratings $T_A = 25^{\circ}\text{C}$ unless otherwise specified

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	20	V
Gate- Source Voltage	V_{GS}	± 12	V
Continuous drain current	I_D	0.75	A
Peak Drain Current, Pulsed ¹⁾	I_{DM}	1.8	A
Power Dissipation ²⁾	P_{tot}	0.15	W
Operating Junction	T_J	$-55 \sim 150$	$^{\circ}\text{C}$
Storage Temperature Range	T_{stg}	$-55 \sim 150$	$^{\circ}\text{C}$

Thermal Characteristics

Parameter	Symbol	Max	Units
Thermal Resistance from Junction to Ambient ²⁾	$R_{\theta JA}$	833	$^{\circ}\text{C/W}$

Note :

1) Pulse width $\leq 100\mu\text{s}$, duty cycle $\leq 1\%$, limited by T_{jmax} .

2) Device mounted on FR-4 substrate PC board, 2ozcopper, with 1-inch square copper plate in still air

Electrical Characteristics at TA = 25°C unless otherwise specified

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D = 250μA	20			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} = 20V , V _{GS} = 0V			1	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±10V , V _{DS} = 0V			±10	μA
Gate-Source Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} , I _D = 250μA	0.35		1.1	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} = 4.5V , I _D = 0.65A		170	330	mΩ
		V _{GS} = 2.5V , I _D = 0.45A		230	400	mΩ
		V _{GS} = 1.8V , I _D = 0.25A		350	750	mΩ
Body-Diode PARAMETERS						
Drain-Source Diode Forward Voltage	V _{DS}	I _S = 0.15A, V _{GS} = 0V			1.2	V
Body Diode Reverse Recovery Time	trr	I _F = 3.6A,		7.5		ns
Body Diode Reverse Recovery Charge	Q _{rr}	di/dt = 100A /μs		2.5		nC
DYNAMIC PARAMETERS						
Forward Transconductance	g _{ts}	V _{DS} = 10V, I _D = 0.8A		1.6		S
Input Capacitance	C _{iss}	V _{GS} = 0V V _{DS} = 16V F = 1MHz		79		pF
Output Capacitance	C _{oss}			13		pF
Reverse Transfer Capacitance	C _{rss}			9		pF
Gate charge total	Q _g	V _{DS} = 10V, V _{GS} = 4.5V I _D = 0.9A		1		nC
Gate to Source Charge	Q _{gs}			0.28		nC
Gate to Drain Charge	Q _{gd}			0.22		nC
Turn-On Delay Time	t _{d(ON)}	V _{DS} = 10V, I _D = 0.5A, R _g = 10Ω, V _{GS} = 4.5V		6.7		ns
Turn-On Rise Time	tr			4.8		ns
Turn-Off Delay Time	t _{d(OFF)}			17.3		ns
Turn-Off Fall Time	tf			7.4		ns

Electrical Characteristics Curves

Fig. 1 Output Characteristic

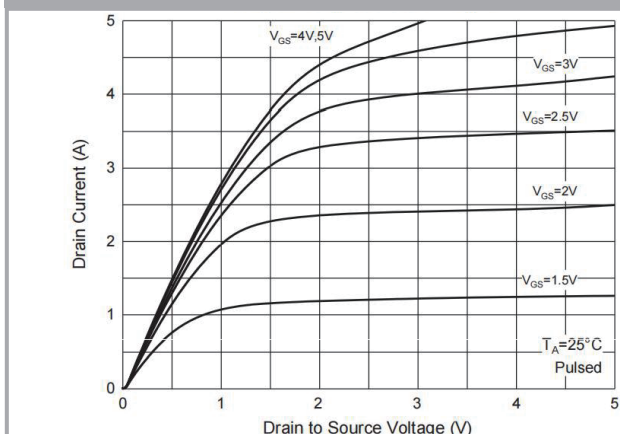


Fig. 2 Transfer Characteristic

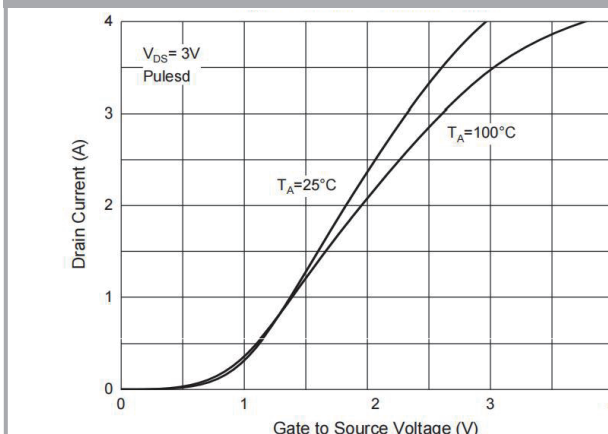


Fig. 3 $R_{DS(ON)} - I_D$

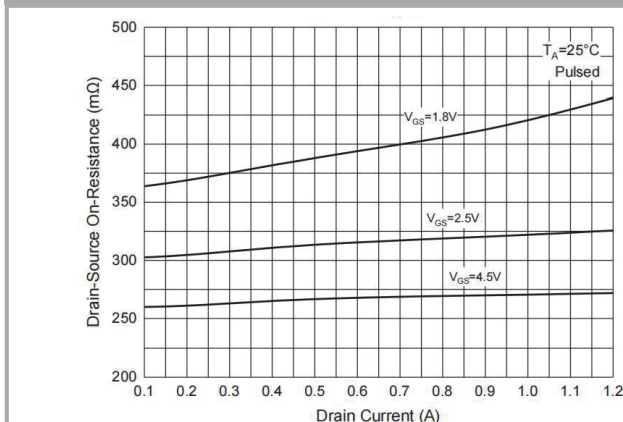


Fig. 4 $R_{DS(ON)} - V_{GS}$

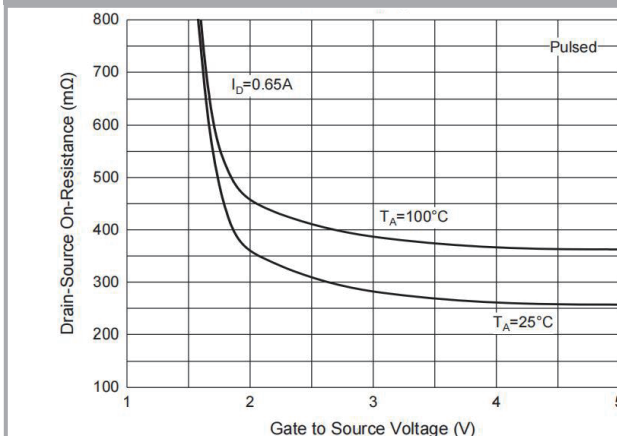


Fig. 5 $I_S - V_{SD}$

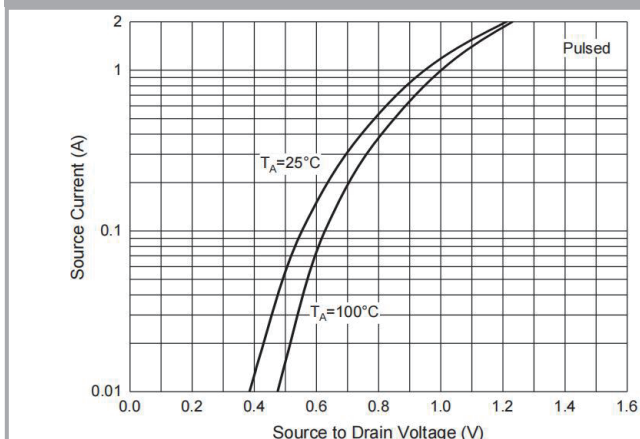
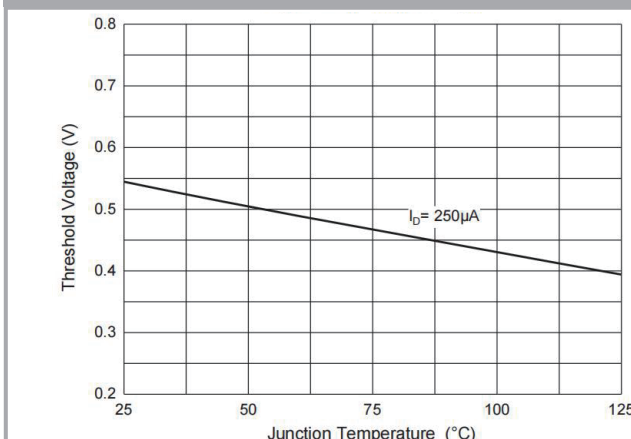


Fig. 6- Threshold Voltage



Test Circuit

Fig.1-1 Switching times test circuit

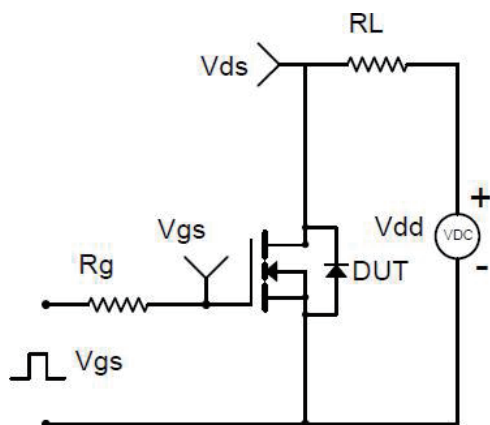


Fig.1-2 Switching Waveform

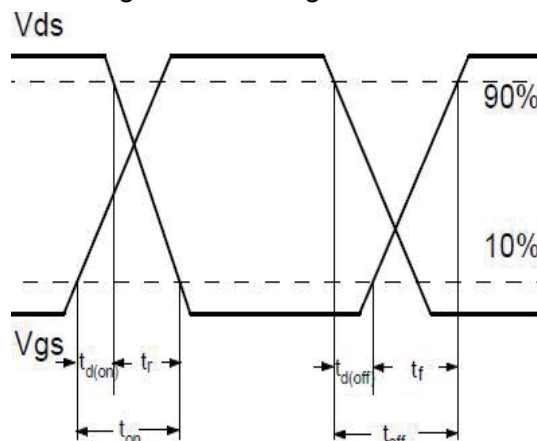


Fig.2-1 Gate charge test circuit

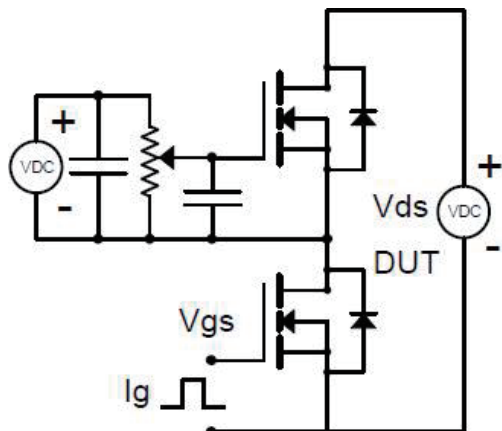


Fig.2-2 Gate charge waveform

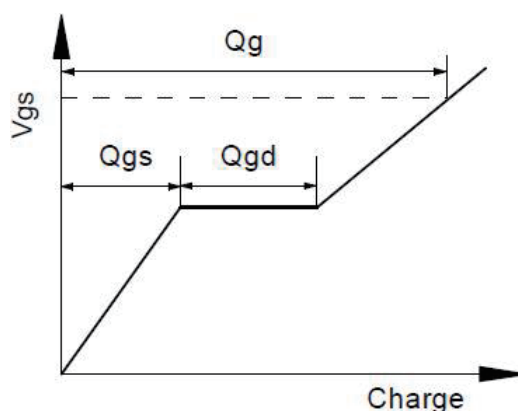


Fig.3-1 Avalanche test circuit

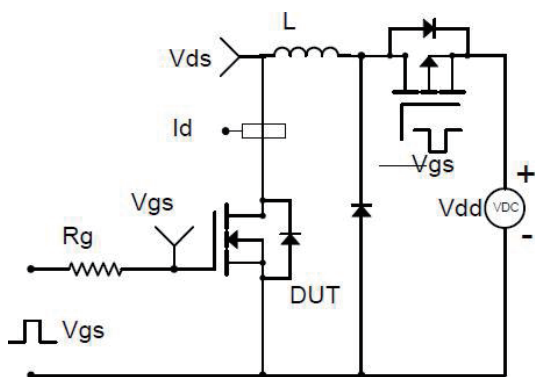
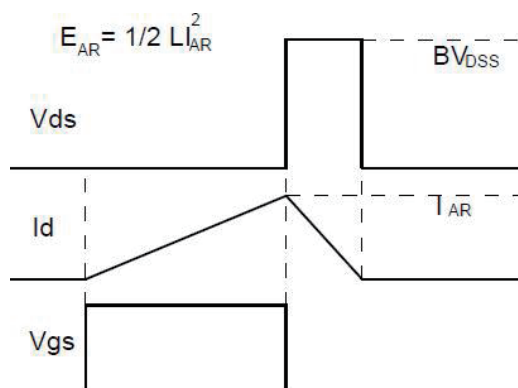
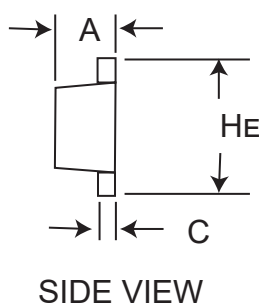
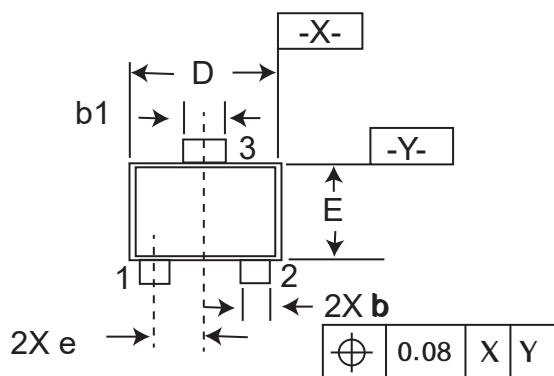


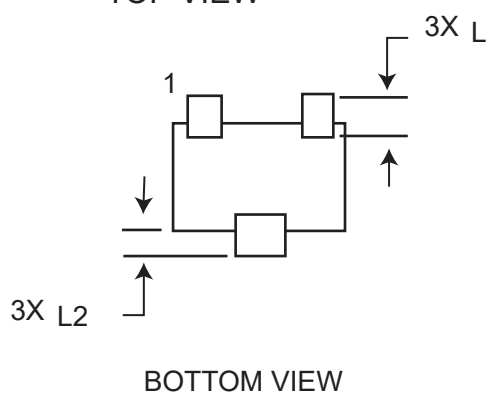
Fig.3-2 Avalanche waveform



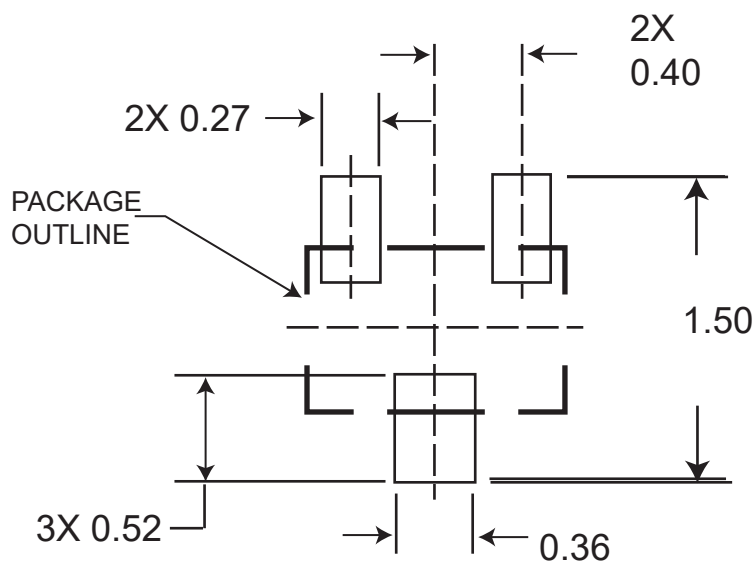
SOT-723 Package Outline & Dimensions (Units: mm / in)



DIM.	Millimeters		
	Min.	Nom.	Max.
A	0.45	0.50	0.55
b	0.15	0.21	0.27
b1	0.25	0.31	0.37
C	0.07	0.12	0.17
D	1.15	1.20	1.25
E	0.75	0.80	0.85
e	0.40BSC		
HE	1.15	1.20	1.25
L	0.29REF		
L2	0.15	0.20	0.25



Soldering Footprint



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