UN0807N5R5-PD56

N-Channel Enhancement Mode MOSFET



Product Summary

Vps	80V
ID(Tc=25°C)	73A
R _{DS(ON)} (@VGS=10V ID=18A)	≤6.5mΩ
R _{DS(ON)} (@VGS=6.0V ID=10A)	≤8.5mΩ

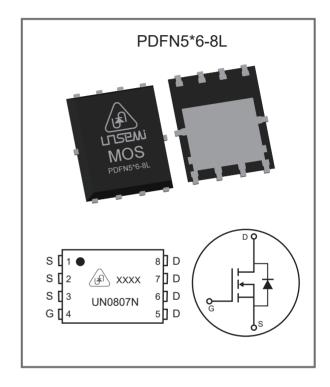
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Features

- ◆ 100% Avalanche Tested
- ◆ Reliable and Rugged
- ◆ RoHS Complian

Applications

- ◆ DC/DC Converter
- ◆ Battery Management System
- ◆ High Power Inverter System
- ◆ Industrial and Motor Drive Applications



Package Marking And Ordering information

Part Number	Package Type	Packaging	Reel(pcs)
UN0807N5R5-PD56	PDFN5x6-8L	Tape & Reel	5,000



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Absolute Maximum Ratings Tc = 25℃ unless otherwise specified

Parameter		Symbol	Max.	Units	
Drain-Source Voltage		VDs	80	V	
Continuous Drain Current 1)	@Tc=25℃	. ID –	60	А	
Continuous Diam Current	@Tc=100°C		46		
Pulsed Drain Current 2)		lом	292	А	
Drain Current of Silicon Wafer 3)		lDsw	73	А	
Gate-Source Voltage		Vgs	±20	V	
Power Dissipation	@Tc=25°C	Pp	62.5	W	
Fower Dissipation	@Tc=100°C		25	VV	
Single Pulse Avalanche Energy 4)		Eas	235	mJ	
Junction and Storage Temperature Range		TJ,Tstg	-55~+150	°C	

Thermal Characteristics

Parameter	Symbol	Тур.	Max.	Units	
Junction-to-Ambient Thermal Resistance	Steady State	RθJA	-	62	°C/W
Junction-to-Case Thermal Resistance	Steady State	Rөjc	-	2.0	°C/W

Notes:

- 1). The maximum current rating is package limited.
- 2). Single pulse width limited by junction temperature.
- 3). The maximum current rating is silicon wafer limited.
- 4).Limited by $T_{_{J(MAX)}}\!,$ starting $T_{J}{=}25^{\circ}\!C$, $Rg{=}25\Omega,$ L=0.5mH.
- 5). Design parameters, Guaranteed by design, not subject to production.





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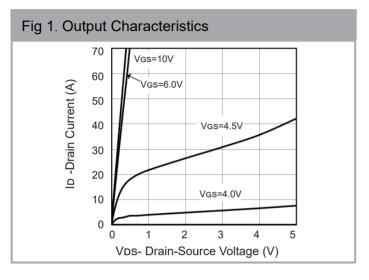
Electrical Characteristics at Tc = 25°C unless otherwise specified

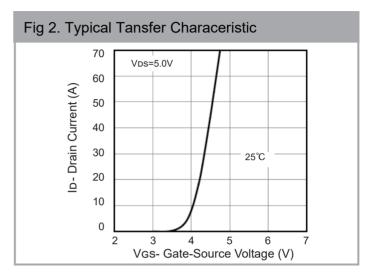
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
STATIC PARAMETERS						
Drain-Source Breakdown Voltage	BVDSS	Vgs = 0V , ID = 250μA	80			V
Drain-Source Leakage Current	IDSS	Vps = 80V , Vgs = 0V			1.0	μΑ
Gate-Source Leakage Current	Igss	Vgs = ±20V , Vps = 0V			±100	nA
Gate Threshold Voltage	VGS(TH)	$VDS = VGS$, $ID = 250\mu A$	2.3		3.7	V
Drain Source On State Posistance	D= - ()	Vgs = 10V , ID = 18A		5.4	6.5	mΩ
Drain-Source On-State Resistance	RDS(ON)	Vgs = 6.0V , ID = 10A		6.5	8.5	
Forward Transconductance	gfs	VDS = 5.0V , ID = 20A		34		S
В	ODY-DIO	DE PARAMETERS				
Drain-Source Diode	VsD	Is = 3.0A , Vgs = 0V		0.8	1.2	V
Reverse Recovery Time	trr	IF = 20A		43.7		nS
Reverse Recovery Charge	Qrr	di/dt = 500A/µs		67.5		nC
	DYNAMI	C PARAMETERS 5)				
Gate Resistance	Rg	F = 1MHz		2.8		Ω
Input Capacitance	Ciss	Vgs = 0V		2886		pF
Output Capacitance	Coss	Vps = 40V F = 1.0MHz		978		pF
Reverse Transfer Capacitance	Crss	1 – 1.0IVII 12		86		pF
Gate Charge Total	Qg	ID = 20A		13.8		nC
Gate to Source Charge	Qgs	Vps = 40V Vgs = 10V		17.0		nC
Gate to Drain Charge	Qgd	VGS - 10V		51.7		nC
SWITCHING PARAMETERS 5)						
Turn-On Delay Time	td(ON)	.,		16.8		nS
Turn-On Rise Time	tr	VDS = 40V VGS =10V		89.3		nS
Turn-Off Delay Time	td(OFF)	$R_G = 3.0\Omega$ $R_L = 2.5\Omega$		32.4		nS
Turn-Off Fall Time	tf	TL - 2.012		86.1		nS

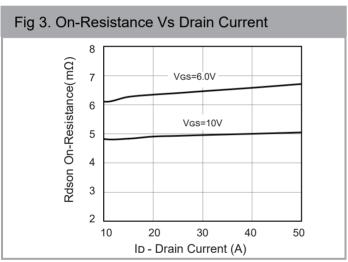


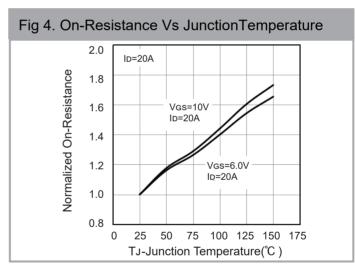
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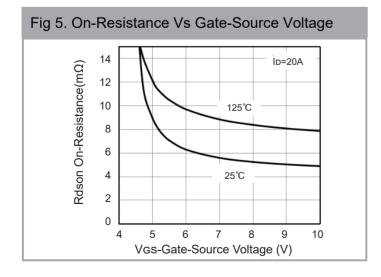
Electrical Characteristics Curves

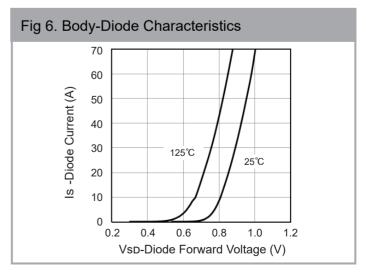








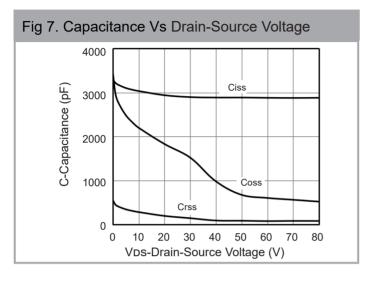


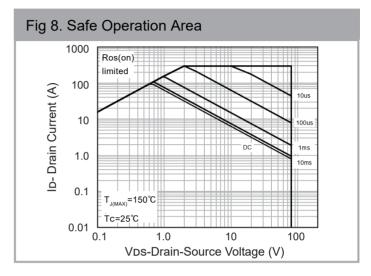


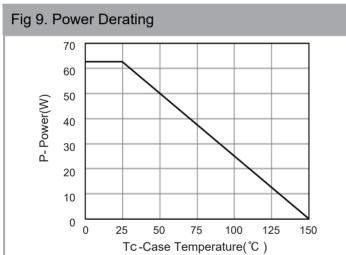


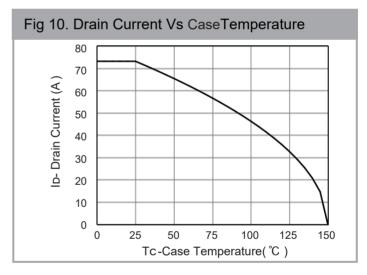
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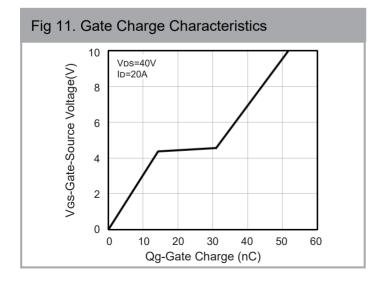
Electrical Characteristics Curves









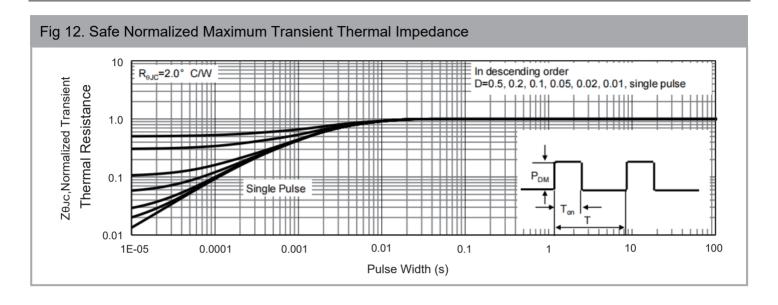






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Electrical Characteristics Curves

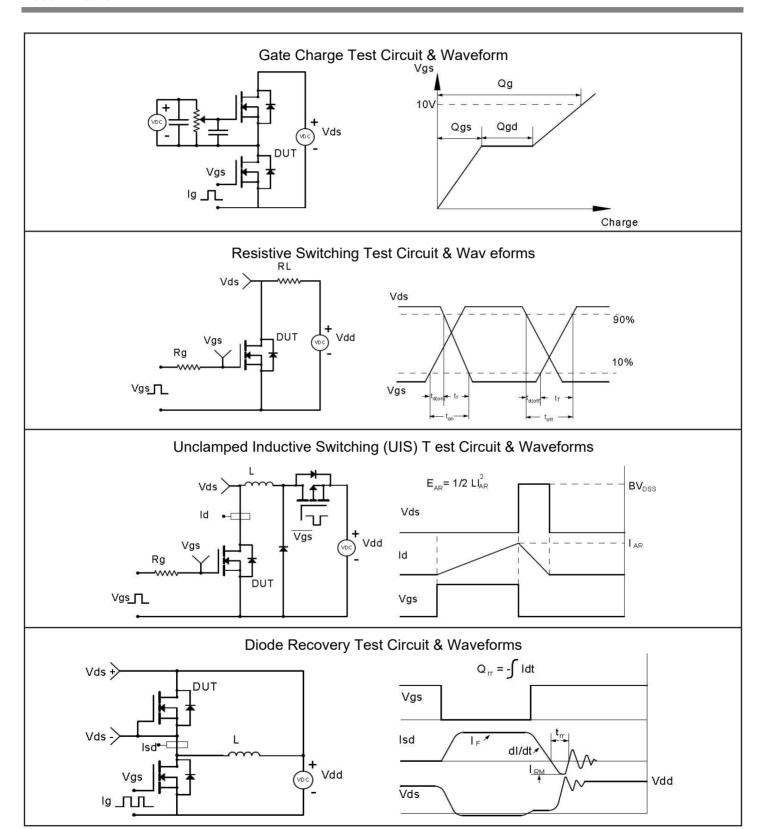






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Test Circuit

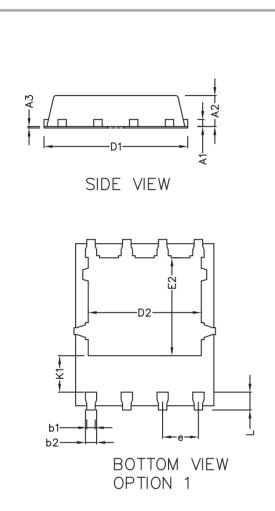




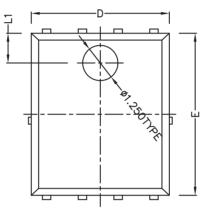


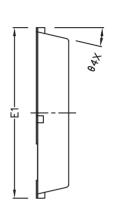
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PDFN5*6-8L Package Outline & Dimensions (Units: mm / in)



PDFN5*6-8L





TOP VIEW

SIDE VIEW

Symbol	Dimensions I	n Millimeters	Dimensions In Inches		
Cymbol	Min	Max	Min	Max	
A1	(0.254	BSC)	(0.0100 BSC)		
A2	1.000	1.100	0.0394	0.0433	
A3	0.005	-	0.0001	-	
b1	0.250	0.300	0.0098	0.0118	
b2	0.350	0.400	0.0138	0.0157	
D	4.800	4.900	0.1890	0.1929	
D1	5.000	5.100	0.1969	0.2008	
D2	3.910	4.010	0.1539	0.1579	
Е	5.650	5.750	0.2224	0.2263	
E1	5.950	6.050	0.2342	0.2381	
E2	3.375	3.475	0.1329	0.1368	
е	(1.270	TYPE)	(0.0500 TYPE)		
L	0.530	0.630	0.0209	0.0248	
L1	1.00 REF		0.0394 REF		
θ	13° TYPE		13° TYPE		
K1	1.235 REF		0.0486 REF		



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